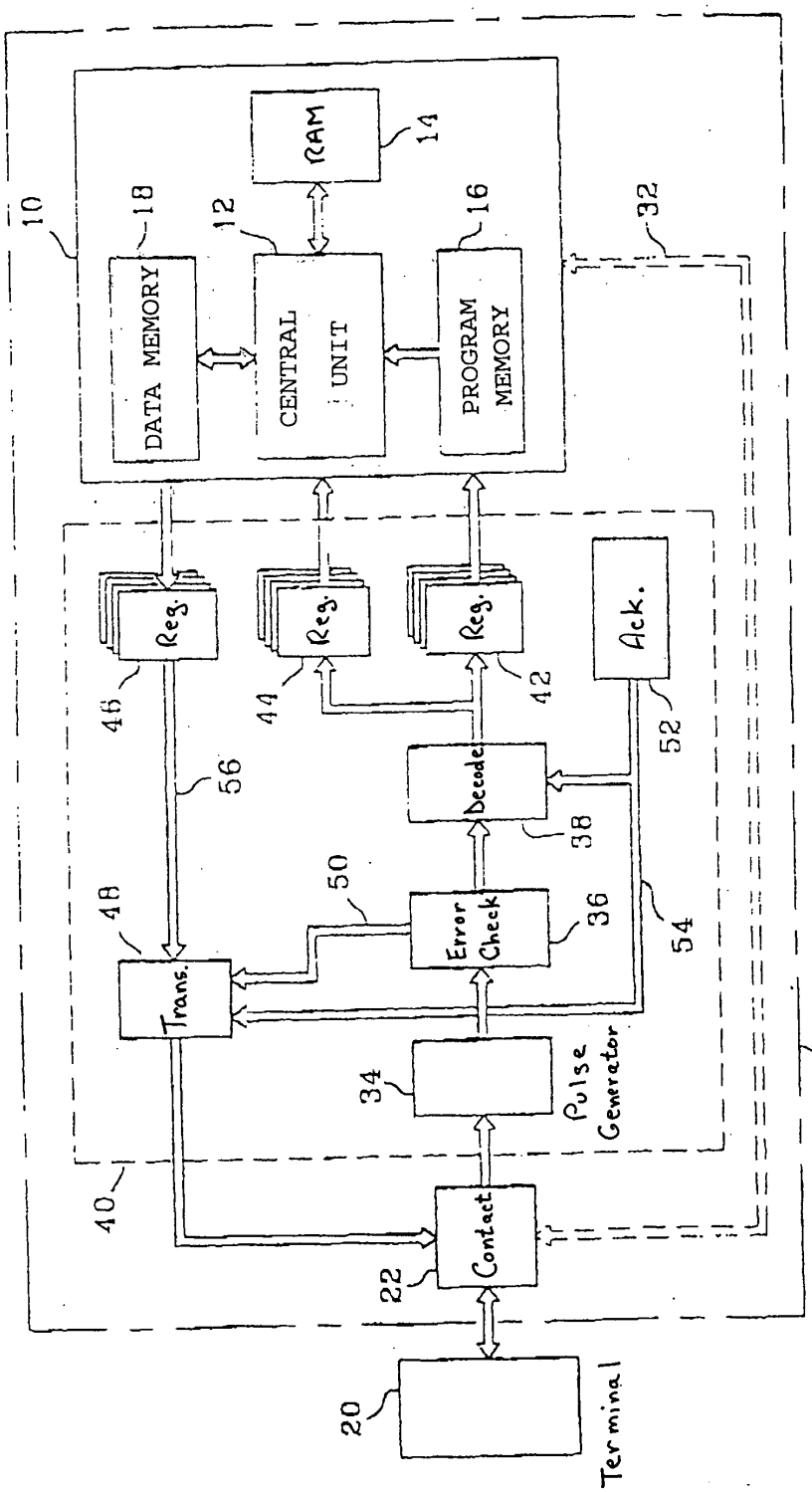
10
RAM
14
18
12
16
32
DATA MEMORY
CENTRAL UNIT
PROGRAM MEMORY
Reg.
Reg.
Reg.
Ack.
46
44
42
52
56
Decode
38
50
48
Error Check
36
54
Trans.
34
Pulse Generator
30
40
Contact
22
20
Terminal

FIG. 1